

Figure 11. PCI Interface

AF004606

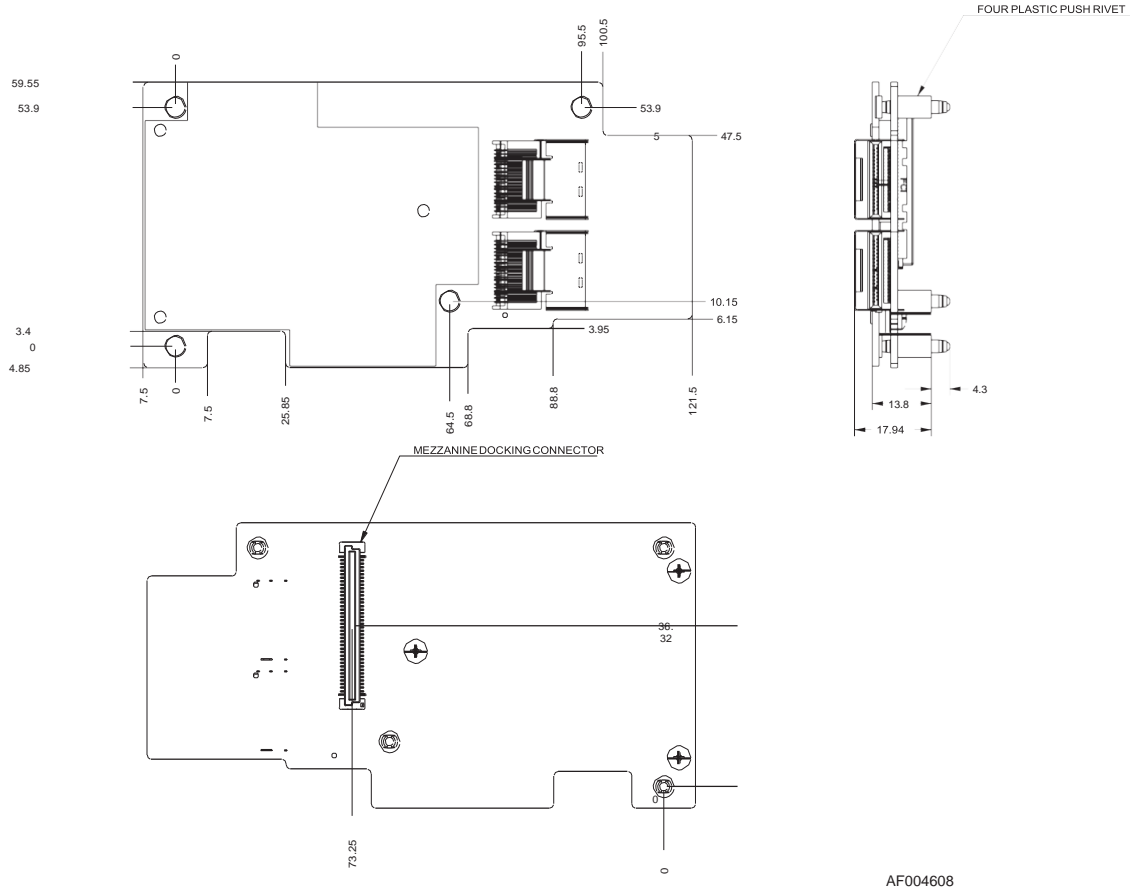
Table 3. Host Board Interface Connector Pin-Out

Pin #	Description	Pin #	Description
1	RSVD_SE	2	GND
3	GND	4	PCIe Gen3 Rp [0]
5	PCIe Gen3 Tp [0]	6	PCIe Gen3 Rn [0]
7	PCIe Gen3 Tn [0]	8	GND
9	GND	10	PCIe Gen3 Rp [1]
11	PCIe Gen3 Tp [1]	12	PCIe Gen3 Rn [1]
13	PCIe Gen3 Tn [1]	14	GND
15	GND	16	PCIe Gen3 Rp [2]
17	PCIe Gen3 Tp [2]	18	PCIe Gen3 Rn [2]
19	PCIe Gen3 Tn [2]	20	GND
21	GND	22	PCIe Gen3 Rp [3]

Pin #	Description	Pin #	Description
23	PCIe Gen3 Tp [3]	24	PCIe Gen3 Rn [3]
25	PCIe Gen3 Tn [3]	26	GND
27	GND	28	PCIe Gen3 Rp [4]
29	PCIe Gen3 Tp [4]	30	PCIe Gen3 Rn [4]
31	PCIe Gen3 Tn [4]	32	GND
33	GND	34	PCIe Gen3 Rp [5]
35	PCIe Gen3 Tp [5]	36	PCIe Gen3 Rn [5]
37	PCIe Gen3 Tn [5]	38	GND
39	GND	40	PCIe Gen3 Rp [6]
41	PCIe Gen3 Tp [6]	42	PCIe Gen3 Rn [6]
43	PCIe Gen3 Tn [6]	44	GND
45	GND	46	PCIe Gen3 Rp [7]
47	PCIe Gen3 Tp [7]	48	PCIe Gen3 Rn [7]
49	PCIe Gen3 Tn [7]]	50	GND
51	GND	52	rSASm REFCLK- [0]
53	SMB DAT	54	rSASm REFCLK+ [0]
55	SMB CLK	56	GND
57	GND	58	PERST#
59	RSVD_DN	60	WAKE#
61	RSVD_DP	62	FM_rINTm_PRESENT_N
63	GND	64	LED_HDD_N
65	RSVD_DN	66	3.3VSTBY
67	RSVD_DP	68	FM_rINTm_MODULE_EN
69	GND	70	5VSB
71	RSVD_SE	72	FRU/TEMP ADDR [A0]
73	3.3V	74	12V
75	3.3V	76	12V
77	3.3V	78	12V
79	3.3V	80	12V

3.1.9 Mechanical Overview

The following figures display the Intel® Integrated RAID Module RMS25JB040 and RMS25JB080 dimensions.



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Figure 12. Mechanical Drawing for Intel® Integrated RAID Module RMS25JB0x0