SPU 二次开发指北 Using SPU in Research

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To Use Customized Protocols

- Situations:
	- new developed sub-functions or a better construction
- Possible ways:
	- Python hijack
	- Intrinsic calls
	- C++ programming

Working Scenario: Evaluating GELU

- Gaussian Error Linear Unit (GELU) $G = 0.5x(1 + \tanh(\sqrt{2/\pi} \cdot (x + 0.044715 \cdot x^3)))$
- By default, SPU will (approximately) evaluate tanh(*) which is expensive to use
- How can we switch to a customized Gelu protocol?
- Difficulty:
	- The Gelu function has been broken into multiple pieces of smaller functions.
	- Pattern matching the Gelu graph from the whole program is tedious
- Solution: Python context hijack

Python Context Hijack

• Replacing the target function on-the-fly

def $program_{\text{1}} that_{\text{2}} use_{\text{2}}ell(x)$: $y = jax.numpy.square(x)$ return jax.nn.gelu $(x + y)$

@contextmanager def hack_context(enabled: bool = True) if not enabled vield return Replacing all the "jax.nn.gelu" $raw_gelu = jax.nn.gelu$ call in the program on-the-fly $java nn.getlu = customized_gelu$ yield jax.nn.gelu = raw_gelu **4**with hack_context(enabled=False): spu_gelu = ppsim.sim_jax(sim, program_that_use_gelu) $z1 = \text{spu_gelu}(x)$ ## Enable hijack: costs 35MB in ABY3 with hack_context(enabled=True):

.
from contextlib import contextmanager

spu_gelu = ppsim.sim_jax(sim, program_that_use_gelu) $z1 = \text{spu_gelu}(x)$

Python Context Hijack

- Replacing the target function on-the-fly
- Good: Barely need to change the given program (e.g,. taking from HuggingFace)
	- NOTE: some (pointer) function is not hijack-able, e.g., activation functions. We still need to change the Python source code.
- Bad:
	- Lack of fine-grained control, replacing none-or-all.
	- No low-level control is possible
	- Still lack of time-profiling for the hijack function (since we are still working above the Op level)

Intrinsic Calls

• A piece of $C++$ codes that can be called directly $\mathcal P$

- Checkout spu/spu/intrinsic at main · secretflow/spu (github.com)
- Entry: spu/libspu/device/pphlo/pphlo_intrinsic_executor.cc at main · secretflow/sp
- Workflow:
	- Run `python spu/intrinsic/add_new_intrinsic.py
		- Will generate some python wrapper in `spu/intrinsic/
		- NOTE: Need manually add
	- Write your C++ codes and add a dispatch in `p
		- May need some config to Bazel build file to compile

Intrinsic Calls

• A piece of $C++$ codes the shape result shape = {inputs

- Checkout spu/spu/intrinsic at main \cdot secretflow auto zeros = kernel::hlo::Com
- Entry: spu/libspu/device/pphlo/pphlo_intrinsical_interaction.isSecret() ||
- Workflow:
	- Run `python spu/intrinsic/
		- Will generate some pythor settlern {zeros};
		- NOTE: Need manually add
	- Write your C++ codes and SPU_ENFORCE(inputs.size() =
		- May need some config to

std::vector<Value> intrinsic_di:

```
FIXME: This should be some
if (name == "example_binary")
 SPDLOG_INFO("Binary example
              inputs[1]);
    zeros = kernel::hlo::Cast
 \} else {
    zeros = kernel::hlo::Cast
```

```
if (name == "customized_gelu"
```

```
if (name == "example") {
  SPDLOG_INFO("Calling example
  return {inputs.begin(), inpu
SPU_THROW("Unhandled intrinsio
```
Intrinsic Calls

Intrinsic Calls from Hijack

- Intrinsic (i.e., Python wrapper) is still a Python call
- Thus can also be used in context hijack

```
from contextlib import contextmanager
import spu.intrinsic as intrinsic
@contextmanager
def hack_context(enabled: bool = True, use_intrinsic: bool = False):
    if not enabled:
        vield
        return
    # hijack some target functions
    raw\_gelu = jax.nn.getuif use_intrinsic:
        jax.nn.gelu = intrinsic.customized_gelu
    else:
        java.nn.getlu = customized\_geluyield
    java.nn.getlu = raw.getlu
```

```
x = np.random.random(100000) * 8with hack_context(enabled=True, use_intrinsic=False):
    spu_gelu = ppsim.sim_jax(sim, program_that_use_gelu)
    z1 = \text{spu\_gelu}(x)
```

```
with hack_context(enabled=True, use_intrinsic=True):
  z1 = \text{spu\_gelu}(x)
```
Improvements due to finegrained truncation control

Intrinsic Calls from Hijack

- Intrinsic (i.e., Python wrapper) is still a Python call
- Thus can also be used in context hijack
- Turn on 'config.enable_pphlo_profile = True' can see the profiling for intrinsic

[api.cc:163] [Profiling] SPU execution program_that_use_gelu completed, input processing took 1.08 [api.cc:191] HLO profiling: total time 0.15015229200000002 [api.cc:196] - pphlo.add, executed 1 times, duration 0.000888167s, send bytes 0 [api.cc:196] - pphlo.custom_call, executed 1 times, duration 0.14534825s, send bytes 28100000 [api.cc:196] - pphlo.free, executed 2 times, duration 7.8333e-05s, send bytes 0 [api.cc:196] - pphlo.multiply, executed 1 times, duration 0.003837542s, send bytes 4000000 cc:1911 HAL profiling: total time 0.149437707000000003

Advanced Topic 1: Full Control

Advanced Topic 1: Full Control

- Intrinsic already provides the access to `SPUContext` from which we can fully control the MPC back-end
	- E.g., `ctx->prot()` to obtain the handler of the underlying MPC back-end
- For example, to compute the square, cubic and quad term from [x]
	- That is $[x] = \int [x^2 3]$, $[x^3 3]$ and $[x^4 4]$.

The cases that we interest, we can call the optimized function

Advanced Topic 2: C++ Programming

Advanced Topic 2: C++ Only Programming

• The C++ module under `libspu/kernel/hal/` can be used directly

```
spu::Value logistic_hessian(spu::SPUContext* ctx, const spu::Value& prob) {
 using namespace spu:: kernel;
 SPU_ENFORCE(prob.isFxp());
 // hessian = (1 - prob) * prob// We explicitly compute prob - prob<sup>1</sup>2 because square is faster than mul.
  spu::Value prob_square = hal::f_mul(ctx, prob, prob);spu::Value hessian = hal::f\_sub(ctx, prob, prob_square);return hessian;
```
Advanced Topic 2: C++ Only Programming

return sym_table.getVar("output");

- For complicated function (e.g., multi-head attention), we can leverage the Python to dump the Intermediate Representation (IR) and call the IR in $C++$ directly.
- Use regex to handle the shape information

pu::Value MaxPool2D(spu::SPUContext* ctx, const spu::Value& tensor, int64_t window_size, int64_t stride) { SPU_ENFORCE_EQ(tensor.shape().ndim(), 4L, "needs 4D tensor NxHxWxC"); $std::string$ jit = $R"$ (func.func_@main(%arg0: tensor<INPUT_SHAPE!pphlo.secret<f32>>) -> tensor<OUTPUT_SHAPE!pphlo.secret<f32>> { %0 = pphlo.constant dense<0xFF800000> : tensor<f32> %1 = pphlo.convert %0 : (tensor<f32>) -> tensor<!pphlo.secret<f32>> pphlo.free %0 : tensor<f32> $\frac{1}{2}$ = "pphlo.reduce_window"(%arg0, %1) ({ ^bb0(%arg1: tensor<!pphlo.secret<f32>>, %arg2: tensor<!pphlo.secret<f32>>): %3 = pphlo.greater %arg1, %arg2 (tensor<!pphlo.secret<f32>>, tensor<!pphlo.secret<f32>>) -> tensor<!pphlo.secret<i1>> %4 = pphlo.select %3, %arg1, %arg2 (tensor<!pphlo.secret<i1>>, tensor<!pphlo.secret<f32>>, tensor<!pphlo.secret<f32>>) -> tensor<!pphlo.secret<f32> pphlo.free %3 : tensor<!pphlo.secret<i1>> pphlo.return %4 : tensor<!pphlo.secret<f32>> }) {window_dilations = $array <$ i64: 1, 1, 1, 1> window_dimensions = array<i64: 1, WIN_SZE, WIN_SZE, 1>, window_strides = $array *is* 64: 1, STRIDE, STRIDE, 1>$ (tensor<INPUT_SHAPE!pphlo.secret<f32>>, tensor<!pphlo.secret<f32>>) -> tensor<OUTPUT_SHAPE!pphlo.secret<f32>> pphlo.free %1 : tensor<!pphlo.secret<f32>> return %2 : tensor<OUTPUT SHAPE!pphlo.secret<f32>> spu::Shape output_shape = tensor.shape(); for (size_t d : {1, 2}) { output_shape[d] = (tensor.shape()[d] - window_size + stride) / stride; jit = std::regex_replace(jit, std::regex("INPUT_SHAPE"), ToPPHLOShape(tensor.shape())); jit = std::regex_replace(jit, std::regex("OUTPUT_SHAPE"), ToPPHLOShape(output_shape)); jit = std::regex_replace(jit, std::regex("WIN_SZE") std::to_string(window_size)) jit = std::regex_replace(jit, std::regex("STRIDE"), std::to_string(stride)); spu::device::SymbolTable sym_table; sym_table.setVar("tensor", tensor); spu::ExecutableProto executable; executable.set_name("maxpool_2d") $executable.set_code(iit);$ std::vector<std::string> input_names = {"tensor"} std::vector<std::string> output_names = {"output"} *executable.mutable_input_names() = {input_names.begin(), input_names.end()}; *executable.mutable_output_names() = {output_names.begin(), $output_names.end()$; spu::device::pphlo::PPHloExecutor executor; spu::device::execute(&executor, ctx, executable, &sym_table);

Advanced Topic 2: Hand Optimizing IR

```
__name__ == "__main__":
You can modify the code below for debug purpose only.
Please DONT commit it unless it will cause build break.
\mathbf{H}^{\prime} H \mathbf{H}config = spu_pb2.RuntimeConfig(protocol=spu_pb2.ProtocolKind.ABY3, field=spu_pb2.FieldType.FM64
config.end_ble_hal_probfile = Trueconfig.endble\_pphlo\_profile = Truesim = ppsim.Simulator(3, config)x = np.random.random(128, 3)spu_fn = ppsim.sim_jax(sim, lambda x: jnp.argv(x, axis=1))spu_f(n(x))print(spu_fn.pphlo)
                   Dump the ArgMax IR 
                       from jax.numpy
```
reducer() $\left\{ \right.$ $88 =$ pphlo.greater $8arg1,$ $8arg3$ # NaN check which is not useful for MPC 89 = pphlo.equal $8arg1$, $8arg1$ $$10 = pphlo.net$ \$9 $\$11 =$ pphlo.or $\$8,$ $\$10$ 812 = pphlo.select 811 , $8arg1$, $8arg3$ # Stable Argmax: return the smaller index # when two values are equal 813 = pphlo.equal $8arg1, 8arg3$ 814 = pphlo.less $8arg2$, $8arg4$ $815 =$ pphlo.and $813,$ 814 $\$16 =$ pphlo.or $\$11$, $\$15$ 817 = pphlo.select 816 , $8arg2$, $8arg4$ pphlo.return %12, %17

Advanced Topic 2: Hand Optimizing IR

Advanced Topic 2: Hand Optimizing IR

```
reducer() \{reducer() \{88 = pphlo.greater 8arg1, 8arg3# NOTE(lwj): We skip the NaN check.
                                                              # NaN check which is not useful for MPC
# When \ln s = rhs, return rhs ignoring the index
                                                              89 = pphlo.equal 8arg1, 8arg1# lhs > rhs
                                                              $10 = pphlo.net $9
%8 = "pphlo.greater" %arg1, %arg3
                                                 simplify\$11 = pphlo.or \$8, \$10# max(lhs, rhs)
                                                              812 = pphlo.select 811, 8arg1, 8arg389 = "pphlo.select" 88, 8arg1, 8arg3# Stable Argmax: return the smaller index
# select the max index
                                                              # when two values are equal
810 = "pphlo.select" 88, 8arg2, 8arg4
                                                              813 = \{ \text{pphlo.equals } 8arg1, 8arg3 \}"pphlo.return" %9, %10
                                                                    pphlo.less %arg2, %arg4
                                                              814\}) {dimensions = dense<AXIS> : tensor<1xi64>}
                                                                     phlo.and %13, %14
                                                                          or %11, %15
                                                                          select %16, %arg2, %arg4
                                                Some "plaintext" ops are 
                                                                           812, 817barely meanngful for MPC
```