

Inverter Inverter & Charger Solar Charge Inverter

Modbus Protocol V1.8



-, Update history

| versi | Updated    | Updated content                              | author | Audit |  |  |
|-------|------------|--|--------|-------|--|--|
| on    | date       |  |        |       |  |  |
| V1.0  | 2019.05.10 | Initial release (HF and power frequency      | Gao Yi | Huang |  |  |
|       |            | share this protocol, there is no             |        | Feng  |  |  |
|       |            | corresponding definition of 0xFFFF fill)     |        |       |  |  |
| V1.1  | 2019.01.21 | New: 4009 address is defined as input        | Gao Yi | Huang |  |  |
|       |            | frequency                                    |        | Feng  |  |  |
| V1.2  | 2020.03.11 | New: 4110~4111 addresses are defined as      | Anson  | Huang |  |  |
|       |            | unique identification codes;                 |        | Feng  |  |  |
|       |            | Fixed: 4112~4199 address bytes;              |        |       |  |  |
|       |            | New: 4327~4615 energy storage inverter       |        |       |  |  |
|       |            | part agreement;                              |        |       |  |  |
|       |            | New: 4568 address is defined as charging     |        |       |  |  |
|       |            | oower setting;                               |        |       |  |  |
|       |            | (Simultaneous for PV and AC charging)        |        |       |  |  |
|       |            | Note: 4000~4326 is the protocol for ordinary |        |       |  |  |
|       |            | inverters;                                   |        |       |  |  |
|       |            | 4000~4615为储能逆变器用协议;                          |        |       |  |  |
| V1.3  | 2020.03.16 | For 4303~4310, 4311~4318, 4319~4326          | Huang  | Huang |  |  |
|       |            | addresses, add descriptions, in which        | Feng   | Feng  |  |  |



|      |            | 4319~4326 is made into hardware, software,   |       |       |
|------|------------|--|-------|-------|
|      |            | and protocol version numbers                 |       |       |
| V1.4 | 2020.05.21 | Modified: Charging state definition in 4332  | Anson | Huang |
|      |            | address;                                     |       | Feng  |
|      |            | Modified: the number of bytes in the address |       |       |
|      |            | from 4334 to 4393;                           |       |       |
|      |            | Modified: fault definition in 4398~4401      |       |       |
|      |            | address;                                     |       |       |
|      |            | Modified: 4402 address definition 'year'     |       |       |
|      |            | added remarks description;                   |       |       |
|      |            | Modified: 4419 address is defined as         |       |       |
|      |            | reserved;                                    |       |       |
|      |            | Modified: Changed the definition of '01' in  |       |       |
|      |            | 4420 address to '0xAA';                      |       |       |
|      |            | Modified: The unit in the 4422 address is    |       |       |
|      |            | changed to 0.1A, and the definition is       |       |       |
|      |            | changed to the total charging current can be |       |       |
|      |            | set;   |       |       |
|      |            | Modified: battery type definition in address |       |       |
|      |            | 4424;  |       |       |
|      |            | Modified: 4438 address is defined as         |       |       |
|      |            | reserved;                                    |       |       |



|      |           | Modified: 0.01Hz in 4442 address;            |         |       |
|------|-----------|--|---------|-------|
|      |           | Modified: 4451 address is defined as         |         |       |
|      |           | reserved;                                    |         |       |
|      |           | Modified: 4547-4549 addresses are defined    |         |       |
|      |           | as reserved;                                 |         |       |
|      |           | Modified: 4562-4563 addresses are defined    |         |       |
|      |           | as reserved;                                 |         |       |
|      |           | Modified: 4568 address is defined as         |         |       |
|      |           | reserved, and the original definition is the |         |       |
|      |           | same as 4422;                                |         |       |
|      |           | Modified: 4600 address is read-only R;       |         |       |
|      |           | Illustrate:                                  |         |       |
|      |           | 1)4000~4326 for unidirectional and power     |         |       |
|      |           | frequency inverters                          |         |       |
|      |           | 2) 4000~4006, 4009, 4108~4301,               |         |       |
|      |           | 4303~4615 are suitable for bidirectional     |         |       |
|      |           | energy storage inverters                     |         |       |
|      |           |  |         |       |
| V1.5 | 2020.10.9 | New: FFH is a universal address code, and    | Huang   | Huang |
|      |           | all slaves respond and return an answer      | Chengch | Feng  |
|      |           | command. The generic address code FFH        | eng     |       |
|      |           | and the current Device ID of the inverter    |         |       |



|      |            | have the same function.                    |        |         |
|------|------------|--|--------|---------|
| V1.6 | 2021.1.11  | Added: 4452 Overvoltage Protection Voltage | Gao Yi | Huang   |
|      |            | 4453 Overvoltage Protection Recovery       |        | Chengch |
|      |            | 4454 Undervoltage alarm recovery voltage   |        | eng     |
| V1.7 | 2021.1.27  | Modified: 4424 Added N13N14 battery type   | Gao Yi | Huang   |
|      |            |  |        | Chengch |
|      |            | Remove NCA and 24V lithium battery types   |        | eng     |
| V1.8 | 2021.10.26 |  | Gao Yi | Huang   |
|      |            | Added: 4112~4120: SN                       |        | Chengch |
|      |            |  |        | eng     |



# 2. Pin Definition:

|          |       | +5V (positive power supply, load capacity not less   |
|----------|-------|--|
|          |       | than 200mA)  |
|          | 2     | A (RS485 bus signal)                                 |
|          | 3     | B (RS485 bus signal)                                 |
|          | 4     | GND (Power Ground/Communication Ground)              |
|          | 5     | NC (idle, no other functions allowed)                |
|          | 6     | CAN_H (CAN bus signal)                               |
| RJ45通讯接口 | 7     | NC (idle, no other functions allowed)                |
|          | 8     | CAN_L (CAN bus signal)                               |
|          | Illus | trate:   |
|          | ->RS  | 5485 初始波特率 9600bps                                   |
|          | ->CA  | AN initial baud rate 500Kbps                         |
|          | -> R  | S485 and CAN can be combined into one physical       |
|          | inter | face, or separated into two physical interfaces      |
|          | -> pi | roducts that do not require a CAN interface, the CAN |
|          | pin o | lefinition can be ignored                            |



## 3. Definition of Agreement:

### 1. Format

| Start    | Address | Feature | Start   | The length  | CPC abool | Closing  |
|----------|---------|---------|---------|-------------|-----------|----------|
| characte | code    | codes   | address | of the data | (ODVTE)   | characte |
| r        | (1BYTE) | (1BYTE) | (2BYTE) | (2BYTE)     | (ZDYIE)   | r        |

### 2. Description

#### 1)起始符:>10ms

2) Address code: 1 byte, range: 01H<sup>~</sup>F7H (decimal 1<sup>~</sup>247), 00H is the broadcast address, all slaves respond, but do not return commands; FFH is the general address code, all slaves respond, return to answer commands. The generic address code FFH and the current Device ID of the inverter have the same function.
3) Function code: 1 byte

| The name of the         | Access data | Feature | Error |
|-------------------------|-------------|---------|-------|
| command                 | type        | codes   | codes |
| Read single or multiple | WORD        | 03H     | 83H   |
| word registers          |             |         |       |
| Write a single word     | WORD        | 06H     | 86H   |
| register                |             |         |       |
| Write consecutive N     | WORD        | 10H     | 90H   |
| word registers          |             |         |       |

4) Start address: 2 bytes

5) Data length: 2 bytes

6) CRC check: 2 bytes, which is the CRC checksum of each byte of the address code, function code and data

7) 结束符: >10ms

Note:

1) The data address and data are 2 bytes, and the high bytes are sent first, followed by the low bytes, while the CRC sends the low bits first, and then the high bits.

2) The error code is that there is an error in the frame data delivered by the server, and the error returned by the client responds to the function code: error code = function code| 80H

3) Description of the exception code

a. 01H -- Unsupported feature codes

b. 02H -- The PDU start address is incorrect or the PDU start address + data length is beyond the legal range

c. 03H -- The data of the read register or the data of the write register is too long

d, 04H -- The client failed to execute the read or write registers

es 05H -- The data check code issued by the server is incorrect

#### 3. Examples

1) Read hold registers

Request:

| description | Number   | command |
|-------------|----------|---------|
|             | of bytes |         |



| Device address  | BYTE | 01H~F7H                                 |
|-----------------|------|---|
| Feature codes   | BYTE | 03H                                     |
| Start address   | WORD | 4000~4059; 4100~4199; 4300~4615;        |
| Number of words | WORD | Ensure that the data read is within the |
| read            |      | range of legitimate addresses           |
| Checksum        | WORD | All of the above byte CRC checksums     |

# Normal Response:

| description       | Number   | command                             |
|-------------------|----------|-------------------------------------|
|                   | of bytes |                                     |
| Device address    | BYTE     | 01H~F7H                             |
| Feature codes     | BYTE     | 03H                                 |
| The length of the | BYTE     | 01H~FAH                             |
| data              |          |                                     |
| Data content      | WORD     | Readout data (high first, low last) |
|                   | WORD     | Readout data (high first, low last) |
| Checksum          | WORD     | All of the above byte CRC checksums |

## Abnormal Response:

| description    | Number   | command                             |
|----------------|----------|-------------------------------------|
|                | of bytes |                                     |
| Device address | BYTE     | 01H~F7H                             |
| Error codes    | BYTE     | 83H                                 |
| Outlier codes  | BYTE     | N(N=1,2,3,4,5)                      |
| Checksum       | WORD     | All of the above byte CRC checksums |

## 2) Write a single register

## Request:

| description    | Number   | command                             |
|----------------|----------|-------------------------------------|
|                | of bytes |                                     |
| Device address | BYTE     | 01H~F7H                             |
| Feature codes  | BYTE     | 06H                                 |
| Start address  | WORD     | 4100~4199; 4300~4615;               |
| Write data     | WORD     | 0000H~FFFFH                         |
| Checksum       | WORD     | All of the above byte CRC checksums |

## Normal response

| description Number |          | command               |
|--------------------|----------|-----------------------|
|                    | of bytes |                       |
| Device address     | BYTE     | 01H~F7H               |
| Feature codes      | BYTE     | 06H                   |
| Start address      | WORD     | 4100~4199; 4300~4615; |
| Write data         | WORD     | 0000H~FFFFH           |



|  | Checksum | WORD | All of the above byte CRC checksums |  |
|--|----------|------|-------------------------------------|--|
|--|----------|------|-------------------------------------|--|

## Abnormal Response:

| description Number |          | command                             |
|--------------------|----------|-------------------------------------|
|                    | of bytes |                                     |
| Device address     | BYTE     | 01H~F7H                             |
| Error codes        | BYTE     | 86H                                 |
| Outlier codes      | BYTE     | N(N=1,2,3,4,5)                      |
| Checksum           | WORD     | All of the above byte CRC checksums |

## 3) Write consecutive n registers

## Request:

| description     | Number   | command                               |
|-----------------|----------|---------------------------------------|
|                 | of bytes |                                       |
| Device address  | BYTE     | 01H~F7H                               |
| Feature codes   | BYTE     | 10H                                   |
| Start address   | WORD     | 4100~4199; 4300~4615;                 |
| Number of write | WORD     |                                       |
| addresses       |          |                                       |
| Bytes written   | BYTE     | Twice the number of write addresses   |
| Data content    |          | Data Written (High Bit First, Low Bit |
|                 |          | Last)                                 |
| •••             |          | Data Written (High Bit First, Low Bit |
|                 |          | Last)                                 |
| Checksum        | WORD     | All of the above byte CRC checksums   |

## Normal Response:

| description     | Number   | command                             |  |  |
|-----------------|----------|-------------------------------------|--|--|
|                 | of bytes |                                     |  |  |
| Device address  | BYTE     | 01H~F7H                             |  |  |
| Feature codes   | BYTE     | 10H                                 |  |  |
| Start address   | WORD     | 4100~4199; 4300~4615;               |  |  |
| Number of write | WORD     | 0001H~007DH                         |  |  |
| addresses       |          |                                     |  |  |
| Checksum        | WORD     | All of the above byte CRC checksums |  |  |

# Abnormal Response:

| description    | Number   | command |
|----------------|----------|---------|
|                | of bytes |         |
| Device address | BYTE     | 01H~F7H |



|                    | Error codes | BYTE | 90H                                 |  |
|--------------------|-------------|------|-------------------------------------|--|
| Outlier codes BYTE |             |      | N(N=1,2,3,4,5)                      |  |
|                    | Checksum    | WORD | All of the above byte CRC checksums |  |



# 4. PDU address allocation table

| PDU<br>address<br>(Decimal) | byt<br>e | rea<br>d<br>/<br>writ<br>e | unit   | description      |                        | data   | analysis                | return<br>data | Parse instance               |
|-----------------------------|----------|----------------------------|--------|------------------|------------------------|--------|-------------------------|----------------|------------------------------|
| 4000                        | 2        | R                          | 0.1V   | Inputvoltage     |                        |        |                         |                | AC input voltage             |
| 4001                        | 2        | R                          | 0.01A  | Input current    |                        |        |                         |                | AC input current             |
| 4002                        | 2        | R                          | 0.1V   | Output voltage   |                        |        |                         |                | Inverter output voltage      |
| 4003                        | 2        | R                          | 0.01A  | Output current   |                        |        |                         |                | Inverter output current      |
| 4004                        | 2        | R                          | 0.01Hz | Output frequency |                        |        |                         |                | Inverter output<br>frequency |
| 4005                        | 2        | R                          | 0.1V   | Battery voltage  |                        |        |                         |                | Battery voltage              |
| 4006                        | 2        | R                          | 0.1℃   | Temperature      |                        |        |                         |                | Internal temperature         |
|                             |          |                            |        |                  |                        | Bit31  | 1: Input UVP            |                | High-frequency               |
|                             |          |                            |        |                  |                        | Bit30  | 1: Input OVP            |                | inverter failure             |
|                             |          |                            |        |                  |                        | Bit29  | 1: Output OPP           |                | BIT31: Input                 |
|                             |          |                            |        |                  |                        | Bit28  | 1:DC/DC overload        |                | undervoltage                 |
|                             |          |                            |        |                  | H -<br>i -<br>g -<br>h | Bit27  | 1: DC/DC OCP(HW)        |                | BIT30: Input                 |
|                             |          |                            |        |                  |                        | Bit26  | 1: BUSOVP               |                | overvoltage                  |
|                             |          |                            |        |                  |                        | Bit25  | 1: PEN                  |                | BIT29: Output overload       |
|                             |          |                            |        |                  |                        | Bit24  | 1: OTP                  |                | BIT28:DC/DC 过载               |
|                             |          |                            |        |                  |                        | Bit23  | 1: Output short circuit |                | BIT27:DC/DC 硬件过流             |
|                             |          |                            |        |                  |                        | Ditao  |                         |                | BIT25: Ground fault          |
|                             |          |                            |        |                  | w                      | Bit22  | 1: Output OVP           |                | BIT24: Other faults          |
|                             |          |                            |        |                  | 0                      | Bit21  | 1: Sleep                |                | BIT23: Output short          |
| 4007~                       |          |                            |        |                  | r                      | Bit19  | reserve                 |                | circuit                      |
| 4008                        | 4        | R                          | /      | Device Status    | d                      | Bit18  | reserve                 |                | BIT22: Output                |
|                             |          |                            |        |                  |                        | Bit17  | reserve                 |                | undervoltage                 |
|                             |          |                            |        |                  |                        |        |                         |                | BIT21: Output                |
|                             |          |                            |        |                  |                        |        |                         |                | overvoltage                  |
|                             |          |                            |        |                  |                        | Bit16  | reserve                 |                | Bit20: Low power             |
|                             |          |                            |        |                  |                        |        |                         |                | hibernation                  |
|                             |          |                            |        |                  |                        | D:41 F | 1. II.:1:4- Doil        |                |                              |
|                             |          |                            |        |                  | L                      | Bit15  | 1: Dunity Fan           |                |                              |
|                             |          |                            |        |                  | 0                      | Bit14  | 1: ADD                  |                |                              |
|                             |          |                            |        |                  | w                      | DI(13  | 1: LIPS File            | UPS            | The power frequency          |
|                             |          |                            |        |                  |                        | DILIZ  |                         | failure        | machine is faulty            |
|                             |          |                            |        |                  | vv                     | D:411  | 1: UPS Type             |                |                              |
|                             |          |                            |        |                  | 0                      | Bit11  | IsLine-Interactive      |                |                              |
|                             |          |                            |        |                  | r                      |        | 0: UPS Type is On line  |                |                              |



|           |   |   |        |               | d | Bit10 | 1: Test in progress |           |                    |
|-----------|---|---|--------|---------------|---|-------|---------------------|-----------|--------------------|
|           |   |   |        |               |   | Bit9  | 1: Shutdown Active  |           |                    |
|           |   |   |        |               |   | Bit8  | 1: Beeper On        |           |                    |
|           |   |   |        |               |   | Bit7  | 1:Fan locked(Inv)   |           |                    |
|           |   |   |        |               |   | Bit6  | 1: Over Load(Inv)   |           |                    |
|           |   |   |        |               |   | Bit5  | 1: Short Cut(Inv)   |           |                    |
|           |   |   |        |               |   | D:+4  | 1:Bat bad(Inv)      | Turnoutou |                    |
|           |   |   |        |               |   | DIL4  | (AC start Vbat<9V)  | 加Verter   |                    |
|           |   |   |        |               |   | Bit3  | reserve             | 叹怿        |                    |
|           |   |   |        |               |   | Bit2  | reserve             |           |                    |
|           |   |   |        |               |   | Bit1  | reserve             |           |                    |
|           |   |   |        |               |   | Bit0  | reserve             |           |                    |
| 4009      | 2 | R | 0.01Hz | I/P Frequency |   |       |                     |           | AC input frequency |
| 4010~4059 |   | R | /      | Reserve       |   |       |                     |           | Reserve a bit      |
|           |   |   |        |               |   |       |                     |           |                    |

|      |   |         |        |                       | 00H                  | DisableTestfor10seconds |      | If the battery voltage is  |
|------|---|---------|--------|-----------------------|----------------------|-------------------------|------|--|
| 4100 | 2 | R/      | /      | Pottory or utility    |                      |                         | 014  | low, the UPS will  |
| 4100 | 2 | W       | /      | Dattery of dunity     | 01H                  | EnableTestfor10seconds  | 0111 | immediately switch the   |
|      |   |         |        |                       |                      |                         |      | mains power supply   |
|      |   | R/      |        |                       | 00H                  | Turn on beep            |      | When the AC output is  |
| 4101 | 2 | W       | /      | TurnOn/Off beep       | 01H                  | Turn off been           | 01H  | abnormal, turn on the  |
|      |   | **      |        |                       | 0111                 |                         |      | buzzer   |
|      |   |         |        |                       | 00H                  |                         |      | 01H: Normal mode<br>02H: Hibernation mode<br>03H: Shut down<br>04H: reserved bit |
|      |   | R/      |        |                       | 01H                  | Normal mode             |      |  |
| 4102 | 2 | W       | /      | Inverter mode         | 02H                  | Sleep mode              |      |  |
|      |   | **      |        |                       | 03H                  | Shutdown                |      |  |
|      |   |         |        |                       | 04H                  | Restore                 |      |  |
| 4103 | 2 | R/<br>W | 0.1min | Shutdown UPS output   | Range from           |                         |      |  |
|      |   |         |        | Delay time            | 0.2~10min            |                         |      |  |
|      |   | R/<br>W | 1min   | After UPS output off, | Range from 0000~9999 |                         |      |  |
| 4104 | 2 |         |        | delay time to turn on |                      |                         |      |  |
|      |   |         |        | UPS output again      |                      |                         |      |  |
|      |   |         |        |                       |                      | Cancel Shutdown         |      |  |
|      |   | R/      |        | Cancel shutdown       | 00H                  | command (UPS in         |      |  |
| 4105 | 2 | w       | /      | command               |                      | shutdown mode)          |      |  |
|      |   |         |        |                       | 01H                  | Turn on UPS output (UPS |      |  |
|      |   |         |        |                       | -                    | in restore wait mode)   |      |  |
| 4106 | 2 | R/      | 0.1V   | Rating Voltage        |                      |                         |      | Rated voltage  |
|      |   | W       |        |                       |                      |                         |      |  |
| 4107 | 2 | R/      | 0.01A  | Rating Current        |                      |                         |      | Current rating   |
|      |   | W       |        |                       |                      |                         |      |  |
| 4108 | 2 | R/<br>W | /      | Inverter Password     |                      |                         |      |  |



| 4109      | 2   | R/<br>W | /      | Device ID                     | 01H~F7H        |                          | Default: 01H                            |
|-----------|-----|---------|--------|-------------------------------|----------------|--------------------------|---|
| 4110~4111 | 4   | R/<br>W | /      | Unique identification<br>code |                |                          | Unique identifiers<br>默认:OxFFFFFFFF     |
| 4112~4120 | 18  | R       | /      | SN                            |                | Data format: ASCII       | For details, see the SN definition file |
| 4121~4199 | 158 | R/<br>W | /      | Reserve                       |                |                          | Reserve a bit                           |
|           |     |         |        |                               |                |                          |   |
| 4300      | 2   | Ð       |        | Inverter fault state          | 00H            | Normal                   | Whether it is in a                      |
| 4300      | 2   | ĸ       |        | mverter launt state           | 01H            | Fault                    | protected state                         |
| 4201      | 0   | р       |        | Inverter Charger action       | 00H            | Inverter is not charging | Whether it is in a                      |
| 4301      | 2   | ĸ       |        | query                         | 01H            | Inverter is charging     | charging state                          |
|           |     |         |        |                               | 0011           | Charger knob is at       |   |
| 4200      | 0   | D       |        | Oleannan lan de taat          | UUH            | 100~500A level           | Current knob                            |
| 4302      | 2   | R       |        | Charger knob test             | 0111           | Chargerknob              | adjustment                              |
|           |     |         |        |                               | 01H            | is at 600~1000A level    |   |
| 4303~4310 | 16  | R       | ASICII | Company Name                  |                |                          | Default: RENOGY                         |
|           |     |         |        |                               |                |                          | Default: Product SKU                    |
| 42114219  | 16  | R       | ASICII | Inverter Model                |                |                          | The version number in                   |
| 4311~4318 | 10  |         |        |                               |                |                          | the product approval                    |
|           |     |         |        |                               |                |                          | letter                                  |
|           |     |         |        |                               |                |                          | Hardware firmware pr                    |
|           |     | R       | ASICII |                               | Formati        |                          | otocol version number                   |
| 4319~4326 | 16  |         |        | Version                       | Format:        |                          | The version number in                   |
|           |     |         |        |                               | XX.XX.14       |                          | the product approval                    |
|           |     |         |        |                               |                |                          | letter                                  |
|           |     |         |        |                               | PV informatior | 1                        |   |
| 1307      | 0   | D       | /      | BatSoc %                      |                |                          | Percentage of battery                   |
| 4021      | 4   | ĸ       | /      | Datote 70                     |                |                          | remaining                               |
|           |     |         |        |                               |                |                          | Charging current, the                   |
| 1308      | 2   | Ð       | 0.14   | ChargeCurr                    |                |                          | current flowing into the                |
| 4020      | 4   | ĸ       | 0.1A   | ChargeCull                    |                |                          | battery, e.g. 500,                      |
|           |     |         |        |                               |                |                          | means 50.0A                             |
| 4329      | 2   | R       | 0.1V   | Pv volt                       |                |                          | PV voltage                              |
|           |     |         |        |                               |                |                          | The buck controller                     |
| 4330      | 2   | R       | 0.1A   | Pv Curr                       |                |                          | outputs inductor                        |
|           |     |         |        |                               |                |                          | current                                 |
| 4331      | 2   | R       | 1W     | Pv charger Power              |                |                          | PV charging power                       |
|           |     |         |        |                               |                | 00H: Charging is not     |   |
|           |     |         |        |                               |                | turned on                | Low 8 bits: (state of                   |
| 4332      | 2   | R       | /      | ChargeState                   | Low Word       | 01H: Constant current    | charge)                                 |
|           |     |         |        |                               |                | charging                 |   |
|           |     |         |        |                               |                | 02H: Constant voltage    |   |



|           |     |         |    |             |                 | charging<br>04H: Float charge<br>06H: Battery activation<br>phase<br>07H: Battery<br>disconnection processing   |   |
|-----------|-----|---------|----|-------------|-----------------|---|---|
| 4333      | 2   | R       | 1W | ChargePower |                 | Stage   | Charging power  |
| 4334~4393 | 120 | R/<br>W | /  | Reserve     |                 |   | obligate  |
|           |     |         | L  | I           | nverter setting | S   |   |
| 4394~4397 | 8   | R       | /  | CurrErrReg  |                 | Faultbits,eachrepresenting a fault, a totalof 64 bits.This register isusedbyinternaldebugging tools.  | The current fault bit   |
| 4398~4401 | 8   | R       | 1  | CurrFcode   |                 | 01: The battery is<br>under-voltaged<br>02: Software protection for<br>battery discharge average<br>current overcurrent<br>03: The battery did not<br>receive the alarm<br>04: Battery undervoltage<br>stop discharge alarm<br>05: Battery overcurrent<br>protection<br>06: Charging overvoltage<br>protection<br>07: Bus overvoltage<br>hardware protection<br>08: Bus overvoltage<br>software protection<br>09: PV overvoltage<br>protection<br>10: Bulk overcurrent<br>software protection<br>11: Bulk overcurrent<br>software protection<br>11: Bulk overcurrent<br>software protection<br>12: The mains power is<br>power-off<br>13: Bypass overload | The current fault code<br>has a total of 4<br>addresses, and each<br>address stores a fault<br>code corresponding to<br>the current fault, and<br>can display 4 fault<br>codes at the same time.<br>0 indicates no fault.<br>For example, there are<br>currently two faults:<br>battery undervoltage<br>and inverter overload.<br>It will be displayed as<br>follows:<br>4398: 01<br>4399: 13<br>4400: 00<br>4401: 00 |



|      |   |            |   |             | protection                  |                      |
|------|---|------------|---|-------------|-----------------------------|----------------------|
|      |   |            |   |             | 15: Inverter overcurrent    |                      |
|      |   |            |   |             | hardware protection         |                      |
|      |   |            |   |             | 16: Inverter overcurrent    |                      |
|      |   |            |   |             | software protection         |                      |
|      |   |            |   |             | 17: Inverter short-circuit  |                      |
|      |   |            |   |             | protection                  |                      |
|      |   |            |   |             | 18: Mains charging          |                      |
|      |   |            |   |             | overcurrent hardware        |                      |
|      |   |            |   |             | protection                  |                      |
|      |   |            |   |             | 19: Bulk radiator           |                      |
|      |   |            |   |             | over-temperature            |                      |
|      |   |            |   |             | protection                  |                      |
|      |   |            |   |             | 20: Inverter radiator       |                      |
|      |   |            |   |             | over-temperature            |                      |
|      |   |            |   |             | protection                  |                      |
|      |   |            |   |             | 21: The fan is faulty       |                      |
|      |   |            |   |             | 22: Memory failure          |                      |
|      |   |            |   |             | 23: The model settings are  |                      |
|      |   |            |   |             | incorrect                   |                      |
|      |   |            |   |             | 24:CmdOff                   |                      |
|      |   |            |   |             | 25: Bus short circuit       |                      |
|      |   |            |   |             | 26: Relay short circuit     |                      |
|      |   |            |   |             | 27: The mains charging      |                      |
|      |   |            |   |             | plate is overheated         |                      |
|      |   |            |   |             | 28: The AC input and        |                      |
|      |   |            |   |             | output are reversed         |                      |
|      |   |            |   |             | 29: Bus undervoltage        |                      |
|      |   |            |   |             | software protection         |                      |
|      |   |            |   |             | 30: The battery capacity is |                      |
|      |   |            |   |             | less than 10% (only when    |                      |
|      |   |            |   |             | connected to the BMS        |                      |
|      |   |            |   |             | host)                       |                      |
|      |   |            |   |             | 31: The battery capacity is |                      |
|      |   |            |   |             | less than 5% (only          |                      |
|      |   |            |   |             | connected to the BMS        |                      |
|      |   |            |   |             | host)                       |                      |
|      |   |            |   |             | 32: Battery low power       |                      |
|      |   |            |   |             | shutdown (only connected    |                      |
|      |   |            |   |             | to BMS host)                |                      |
|      |   | <b>D</b> ( |   |             |                             | 8th place high: year |
| 4402 | 2 | К/<br>     | / | SysDateTime |                             | (20 for 2020)        |
|      |   | W          |   |             |                             | Bottom 8 bits: Month |
| 4403 | 2 | R/         | / | SysDateTime |                             | High 8 bits: Day     |
|      |   |            |   |             |                             |                      |



|      |   | W  |       |                                |                |   | <br>Low 8 bits: hours   |
|------|---|----|-------|--------------------------------|----------------|---|---|
| 4404 | 2 | R/ | /     | SysDateTime                    |                |   | High 8 digits: points   |
| +404 | 4 | W  | /     | SysDaterinit                   |                |   | <br>8 bits lower: seconds   |
| 4405 | 2 | R  | /     | MachineState                   |                | 00: power-on delay01: Waiting state02: Initialization03: Soft start04: Mains operation05: Inverter operation06: Inverter to mains07: Mains power toinverter08: Reserved09: Reserved10: Shut down11: Breakdown | The current state of the machine                                  |
| 4406 | 2 | R  | 1     | PriorityFlag                   |                | <ul> <li>0: The user did not enter a password</li> <li>1: The user password has been entered</li> <li>4: The manufacturer's password has been entered</li> </ul>  | Password protection<br>status flags                               |
| 4407 | 2 | R  | 0.1V  | BusVolt                        |                |   | <br>Bus voltage   |
| 4408 | 2 | R  | 0.1A  | Load Curr                      |                |   | Load current  |
| 4409 | 2 | R  | 1W    | Load Active Power              |                |   | Load active power   |
| 4410 | 2 | R  | 1W    | Load Reactive Power            |                |   | Load apparent power   |
| 4411 | 2 | R  | 1mV   | Inv Dc Volt                    |                |   | Inverter DC component   |
| 4412 | 2 | R  | 0.1A  | Line Chg Curr                  |                |   | When the mains is<br>charged, the current on<br>the battery side. |
| 4413 | 2 | R  | %     | Load Ratio                     |                |   | Percentage of load  |
| 4414 | 2 | R  | 0.1°C | Temper it                      |                |   | DC-DC heatsink temperature  |
| 4415 | 2 | R  | 0.1℃  | Temper-b                       |                |   | DC-AC heatsink temperature  |
| 4416 | 2 | /  | /     | /                              |                |   | obligate  |
|      |   |    | •     |                                | Switch control |   |   |
| 4417 | 2 | In | /     | Cmd Machine Reset              |                | 1: Reset<br>Other: No action  | <br>Reset control   |
| 4418 | 2 | In | /     | Cmd Restore Factory<br>Setting |                | OxAA: Recovery,<br>Other: No action<br>Restore the factory value<br>to clear all accumulated  | Restore factory values  |



|         |      |       |       |                       |                 | informatio | on, restore the     |         |                        |
|---------|------|-------|-------|-----------------------|-----------------|------------|---------------------|---------|------------------------|
|         |      |       |       |                       |                 | paramete   | rs to the default   |         |                        |
|         |      |       |       |                       |                 | state, and | l the restart takes |         |                        |
|         |      |       |       |                       |                 | effect     |                     |         |                        |
| 4419    | 2    |       |       | Reserve               |                 |            |                     |         |                        |
| 4.400   | 0    | In    | 1     | Cred Clean His Decard |                 | 0xAA: Cle  | ear                 |         | Clear history          |
| 4420    | 2    | III   | /     | Cind Clear His Record |                 | Other: No  | action              |         | Clear mistory          |
| 4.40.1  | 0    | Ta    | 1     | Batt Equal Chg        |                 | 0: Prohibi | ited                |         | Immediately equalize   |
| 4421    | 4    | III   | /     | Immediate             |                 | 1: Enable  | :                   |         | the charging command   |
|         |      |       |       | Batt                  | ery-related set | tings      |                     |         |                        |
| 4.400   | 0    | R/    | 0.14  | De Cher Green Get     |                 |            |                     |         | The total charging     |
| 4422    | 2    | W     | 0.1A  | Pv Chg Curr Set       |                 |            |                     |         | current is settable    |
|         |      | R/    |       |                       |                 |            |                     |         |                        |
| 4423    | 2    | W     | 1AH   | Bat Rate Cap          |                 |            |                     |         | Battery rated capacity |
|         |      |       |       |                       |                 | 00: User   | Def                 |         |                        |
|         |      |       |       |                       |                 | 01: SLD    |                     |         |                        |
|         |      |       |       |                       |                 | 02: FLD    |                     |         |                        |
|         |      |       |       |                       |                 | 03: GEL    |                     |         |                        |
| 4424    | 2    | R/    | /     | BatTypeSet            |                 | 04:LFP14   | (48v)               |         | Battery type           |
|         |      | W     |       |                       |                 | 05:LFP15   | (48v)               |         |                        |
|         |      |       |       |                       |                 | 06:LFP16   | (48v)               |         |                        |
|         |      |       |       |                       |                 | 0C+ N13    | ()                  |         |                        |
|         |      |       |       |                       |                 | 0D· N14    |                     |         |                        |
|         |      | Rea   |       |                       |                 | 0D. NII    |                     |         |                        |
|         |      | d     |       |                       |                 |            |                     |         |                        |
| addross | buto | u     | unit  | English nome          |                 | maxim      | minimum             | Default | romorte                |
| auuress | byte | urrit | um    | English hame          |                 | um         | mmmum               | value   | Temark                 |
|         |      | witt  |       |                       |                 |            |                     |         |                        |
|         |      | P/    |       |                       |                 |            |                     |         | Faultize the charge    |
| 4425    | 2    | W/    | 0.1V  | Bat Const Chg Volt    |                 | 9.0V       | 15.5V               | 14.4V   | voltage                |
|         |      | vv    |       |                       |                 |            |                     |         | Increase the charge    |
| 1106    | 0    | R/    | 0.117 | Det Immun Ohn Velt    |                 | 0.017      |                     | 14 457  | merease me charge      |
| 4420    | 2    | W     | 0.1V  | Bat Improv Cing voit  |                 | 9.00       | 15.5v               | 14.40   | voltage/overcharge     |
|         |      |       |       |                       |                 |            |                     |         | Flast                  |
|         | 0    | R/    | 0.117 |                       |                 | 0.017      |                     | 14.017  | Float charge           |
| 4427    | 2    | W     | 0.1V  | Bat Float Chg Volt    |                 | 9.00       | 15.5V               | 14.00   | voltage/overcharge     |
|         |      |       |       |                       |                 |            |                     |         | return voltage         |
| 4428    | 2    | R/    | 0.1V  | Bat Improv Chg Back   |                 | 9.0V       | 15.5V               | 13.2V   | Boost the charge       |
|         |      | W     |       | Volt                  |                 |            |                     |         | return voltage         |
| 4429    | 2    | R/    | 0.1V  | Bat Over Dischg Back  |                 | 9.0V       | 15.5V               | 12.6V   | Over-discharge return  |
|         |      | W     |       | Volt                  |                 |            |                     |         | voltage                |
| 4430    | 2    | R/    | 0.1V  | Bat Under Volt        |                 | 9.0V       | 15.5V               | 11.0V   | Undervoltage warning   |
|         |      | W     |       |                       |                 | -          |                     |         | voltage                |
| 4431    | 2    | R/    | 0.1V  | Bat OverDischg Volt   |                 | 9.0V       | 15.5V               | 12.2V   | Over-discharge voltage |
|         | _    | W     |       |                       |                 | 2.01       | 20.07               |         |                        |



| 4432 | 2 | R/<br>W | 0.1V   | Bat Dischg Limit Volt        |                 | 9.0V  | 15.5V   | 11.2V  | Discharge limiting voltage       |
|------|---|---------|--------|------------------------------|-----------------|---|---|--------|----------------------------------|
| 4433 | 2 | R/<br>W | 1s     | Bat OverDischg Delay<br>Time |                 | 0S  | 1205  | 60S    | Over-discharge delay<br>time     |
| 4434 | 2 | R/<br>W | 1min   | Bat Const Chg Time           |                 | 0min  | 600min  | 120min | Equalize the charging time       |
| 4435 | 2 | R/<br>W | 1min   | Bat Improv Chg Time          |                 | 10min   | 600min  | 120min | Improves charging time           |
| 4436 | 2 | R/<br>W | 1day   | Bat Const Chg Gap Time       |                 | 0Day  | 255Day  | 30Day  | Equalize the charging interval   |
| 4437 | 2 | R/<br>W | 0.1V   | Bat Switch DC Volt           |                 | 36.0V   | 62V   | 46V    | Mains switching<br>voltage       |
| 4438 | 2 |         |        | Reserve                      |                 |   |   |        |                                  |
| 4439 | 2 | R/<br>W | 0.1V   | Batt Volt Sw To Inv          |                 | 36V   | 62V   | 64.0V  | Inverter switching<br>voltage    |
| 4440 | 2 | R/<br>W | 1min   | Batt Equal Chg Time out      |                 | 5min  | 900min  | 240min | Equalize the charge timeout      |
|      | • | •       |        | I                            | nverter setting | s   |   |        |                                  |
| 4441 | 2 | R/<br>W | /      | Output Priority              |                 | 0:solar 1:  | line 2:sbu  |        | Output priority                  |
| 4442 | 2 | R/<br>W | 0.01Hz | Output Freq Set              |                 |   |   |        | Output frequency                 |
| 4443 | 2 | R/<br>W | /      | Ac Volt Range                |                 | 0: wide ra<br>1: Narrow   | ange<br>v range   |        | AC input range                   |
| 4444 | 2 | R/<br>W | /      | Power Saving Mode            |                 | 0: Prohib<br>1: Enable  | ited  |        | Energy saving mode               |
| 4445 | 2 | R/<br>W | /      | Auto Restart Ov Load         |                 | 0: Prohib<br>1: Enable  | ited  |        | Automatic restart of overload    |
| 4446 | 2 | R/<br>W | /      | Auto Restart Ov Temper       |                 | 0: Prohib<br>1: Enable  | ited  |        | Over-temperature<br>auto-restart |
| 4447 | 2 | R/<br>W | 1      | Chg Source Priority          |                 | <ul> <li>PV is inv<br/>charging</li> <li>Mains<br/>only sta<br/>charging<br/>is invalid</li> <li>Mixed</li> <li>PV charg<br/>time, priot</li> <li>Photomains</li> <li>charged.</li> </ul> | ralid, start mains<br>a power priority,<br>art photovoltaic<br>when the mains<br>mode, mains and<br>ging at the same<br>ority PV.<br>tovoltaics only,<br>power is not |        | Charging priority                |
| 4448 | 2 | R/      | /      | Alarm Enable                 |                 | 0: Prohib   | ited  |        | Alarm control                    |



| -          |     |         |              |                        |                   |                             |                         |
|------------|-----|---------|--------------|------------------------|-------------------|-----------------------------|-------------------------|
|            |     | W       |              |                        |                   | 1: Enable                   |                         |
|            |     | R/      |              | Alarm En When Source   |                   | 0. Prohibited               | Alarm is enabled when   |
| 4449       | 2   | w       | /            | Loss                   |                   | 1: Enable                   | the input source is     |
|            |     | vv      |              | 1033                   |                   | 1. Enable                   | interrupted             |
|            |     | р/      |              | Prin Enchlo Whon Or    |                   | 0. Drahihitad               | Overland hypers         |
| 4450       | 2   | к/<br>W | /            | Byp Enable when Ov     |                   |                             | Overload bypass         |
|            |     | w       |              | Load                   |                   | 1: Enable                   | enabled                 |
| 4451       | 2   | R       | /            | Reserve                |                   |                             | Pending Position        |
| 4452       | 2   | R       | /            |                        |                   |                             | Overvoltage protection  |
| 1102       | 4   | IX.     | /            |                        |                   |                             | voltage                 |
| 1152       | 0   | D       | ,            |                        |                   |                             | Overvoltage protection  |
| 4400       | 2   | ĸ       | /            |                        |                   |                             | recovery voltage        |
|            |     |         |              |                        |                   |                             | The undervoltage        |
| 4454       | 2   | R       | /            |                        |                   |                             | alarm recovers the      |
|            |     |         |              |                        |                   |                             | voltage                 |
|            |     | R/      |              | _                      |                   |                             |                         |
| 4455~4500  | 92  | W       | /            | Reserve                |                   |                             | obligate                |
|            |     |         |              | Ele                    | ectricity statist | ics                         |                         |
|            |     |         |              |                        |                   |                             | Historical data of PV   |
|            | 14  | R       | 1AH          | PV Energy Last 7day    |                   |                             | power generation for    |
| 4501~4507  |     |         |              |                        |                   |                             | the last 7 days         |
|            |     | R       | 1AH          |                        |                   |                             | Historical data of      |
|            | 14  |         |              | Bat Chg Energy Last    |                   |                             | battery charge in the   |
| 4508~14    |     |         |              | 7day                   |                   |                             | last 7 days             |
| 1000 11    |     | P       | 1 <b>4</b> H |                        |                   |                             | Historical data of      |
|            | 14  | IX.     | 17111        | Bat DisChg Energy Last |                   | 00: Vesterday's electricity | hottery discharge level |
| 4515~4521  | 17  |         |              | 7day                   |                   | generation                  | in the last 7 days      |
| 1010 1021  |     | P       | 1 <b>4</b> H |                        |                   | 01: The day before          | Historical data of the  |
|            | 14  | ĸ       | IAII         | Line Chg Energy Last   |                   | vostordov's electricity     | last 7 days of mains    |
| 45004509   | 14  |         |              | 7day                   |                   | generation                  | hattom charging         |
| 4322**4328 |     | р       |              |                        |                   | generation                  | Uniterioral data of the |
|            | 1.4 | R       | 0.1KW        |                        |                   |                             | Historical data of the  |
| 1500 1505  | 14  |         | Н            | Load Consum Last 7 day |                   |                             | last 7 days of load     |
| 4529~4535  |     | P       |              |                        |                   |                             | consumption             |
|            |     | к       | 0            |                        |                   |                             | Historical data of the  |
|            | 14  |         | 0.1KW        | Load Consum From Line  |                   |                             | last 7 days of load     |
|            |     |         | Н            | Last 7day              |                   |                             | consumption from        |
| 4536~4542  |     |         |              |                        |                   |                             | mains                   |
|            |     | R       |              |                        |                   |                             | Ampere-hours of         |
|            | 2   |         | 1AH          | Break the bat          |                   |                             | battery charging on the |
| 4543       |     |         |              |                        |                   |                             | same day                |
|            |     | R       |              |                        |                   |                             | The number of           |
|            | 2   |         | 1AH          | Bat Dischg AH Todav    |                   |                             | ampere-hours the        |
|            |     |         |              |                        |                   |                             | battery is discharged   |
| 4544       |     |         |              |                        |                   |                             | on the same day         |



| 4546     R     0.1KW     Used Energy Today     Electricity   | :    |
|--|------|
| 4546 0.1KW Used Energy Today H day of load   | :    |
| 4546 H day of load   |      |
|  |      |
| 4547~4549 6 Reserve  |      |
| R The cumulative   |      |
| 4 1AH Bet Chag Ah Total charging ampere-hor  | urs  |
| 4550~4551 of the battery   |      |
| R The cumulative   |      |
| number of  |      |
| 4 1AH Bat Dischg AH Total ampere-hours of  |      |
| 4552~4553 discharge of the batte   | ery  |
| R 0.1KW Cumulative PV powe   | er   |
| 4     Generat Energy Total       4554~4555     H   generation  |      |
| R Accumulated power  |      |
| 4 0.1KW<br>Used Energy Total consumption of the  |      |
| 4556~4557 H load   |      |
| R R The amount of  |      |
| electricity charged or   | n    |
| 2 IAH Line Chg Energy Tday the same day as the   | :    |
| 4558 mains power   |      |
| R O IVIN The load consumes   |      |
| 2 0.1KW Load Consum Line Tday electricity from the   |      |
| 4559 mains on the same of main | day  |
| R Imin Inv Work Time Teday Working hours on th   | ne   |
| 4560 day of the inverter   |      |
| R Bypass working hou   | ırs  |
| 4561 2 Annual Diffe Work Time Todya on the day   |      |
| 4562~4563 4 R Reserve  |      |
| R 0.1KW The load cumulative  | ely  |
| 4     H     Load Consum Line Total     consumes electricity  | 7    |
| 4564~4565 from the mains   |      |
| R Cumulative working   | 5    |
| 4566 hours of the inverter   | r    |
| R Cumulative working   | 5    |
| 4567 hours for bypasses  |      |
| 4568~4599 $\begin{array}{c} 64 \\ W \end{array}$ $\begin{array}{c} R/ \\ W \end{array}$ $\begin{array}{c} / \\ Reserve \end{array}$ Reserve obligate   |      |
| Fault logging  |      |
| Each fault record  |      |
| 0 occupies 16 address  | ses, |
| and a total of 16 fau  | ılt  |
| records are stored.  |      |

| R | ENO | GY |  |  |  |  |                          |  |  |
|---|-----|----|--|--|--|--|--------------------------|--|--|
|   |     |    |  |  |  |  | Fault Record Internal    |  |  |
|   |     |    |  |  |  |  | Data Format              |  |  |
|   |     |    |  |  |  |  | Definition: (Defined by  |  |  |
|   |     |    |  |  |  |  | Internal Offset          |  |  |
|   |     |    |  |  |  |  | Address)                 |  |  |
|   |     |    |  |  |  |  | 0x00: Fault code, the    |  |  |
|   |     |    |  |  |  |  | specific definition of   |  |  |
|   |     |    |  |  |  |  | fault code is shown in   |  |  |
|   |     |    |  |  |  |  | the instruction          |  |  |
|   |     |    |  |  |  |  | manual. If the value of  |  |  |
|   |     |    |  |  |  |  | the fault code is 0, the |  |  |
|   |     |    |  |  |  |  | fault record is invalid. |  |  |
|   |     |    |  |  |  |  | 0x01~0x03: The time      |  |  |
|   |     |    |  |  |  |  | when the fault code      |  |  |
|   |     |    |  |  |  |  | occurs                   |  |  |
|   |     |    |  |  |  |  | 0x04~0x0F: A total of    |  |  |
|   |     |    |  |  |  |  | 12 data packets          |  |  |
|   |     |    |  |  |  |  | captured at the time of  |  |  |
|   |     |    |  |  |  |  | the failure.             |  |  |